



4-20-05 3:57PM

## PART B - FEE(S) TRANSMITTAL

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APR 20 2005  
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7590 02/23/2005

J.C. Patents, Inc  
Suite 250  
4 Venture  
Irvine, CA 92618

04/21/2005 MGEBREM2 00000048 500710 10643675

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JIWEI HUANG		(Depositor's name)
		(Signature)
APRIL 20, 2005		(D)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/643,675	08/15/2003	Yoyi Gong	JCLA11488	4621

TITLE OF INVENTION: METHOD FOR FABRICATING TRANSISTOR HAVING FULLY SILICIDED GATE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/23/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
THOMPSON, CRAIG	2813	438-592000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
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1 \_\_\_\_\_  
2 J.C. PATENTS  
3 \_\_\_\_\_

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

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UNITED MICROELECTRONICS CORP.

HSINCHU, TAIWAN

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.  b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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JIWEI HUANG

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